

3a
2-20-02
ed

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

TI-30100

Douglas Deao, et al.

Examiner: TBD

Serial No: TBD

Art Unit: TBD

Filed: Herewith

For: Multiprocessor Emulation Support Using Dynamic Linking

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D. C. 20231

"EXPRESS MAILING" Mailing Label No. EL645450540US I hereby certify that this paper is being deposited with the U.S. Postal Service Express Mail Post Office to Addressee Service under 37 CFR 1.10 on the date shown below and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

Robert L. Troike 6/22/01
Robert L. Troike, Reg. No. 24,183 Date

Dear Sir:

This application claims priority under 35 USC 119 (e)(1) of provisional application number 60/223,697, filed 08/08/00.

Prior to the examination of the above-identified application, please amend the specification by inserting before the first line the sentence:

--This application claims priority under 35 USC § 119(e)(1) of provisional application number 60/223,697, filed 08/08/00.--

Respectfully submitted,

Robert L. Troike
Robert L. Troike
Attorney for Applicants
Reg. No. 24,183

Texas Instruments Incorporated
P. O. Box 655474, MS 3999
Dallas, TX 75265
(202) 639-7710
Fax: (202) 639-7890

102290-1052880